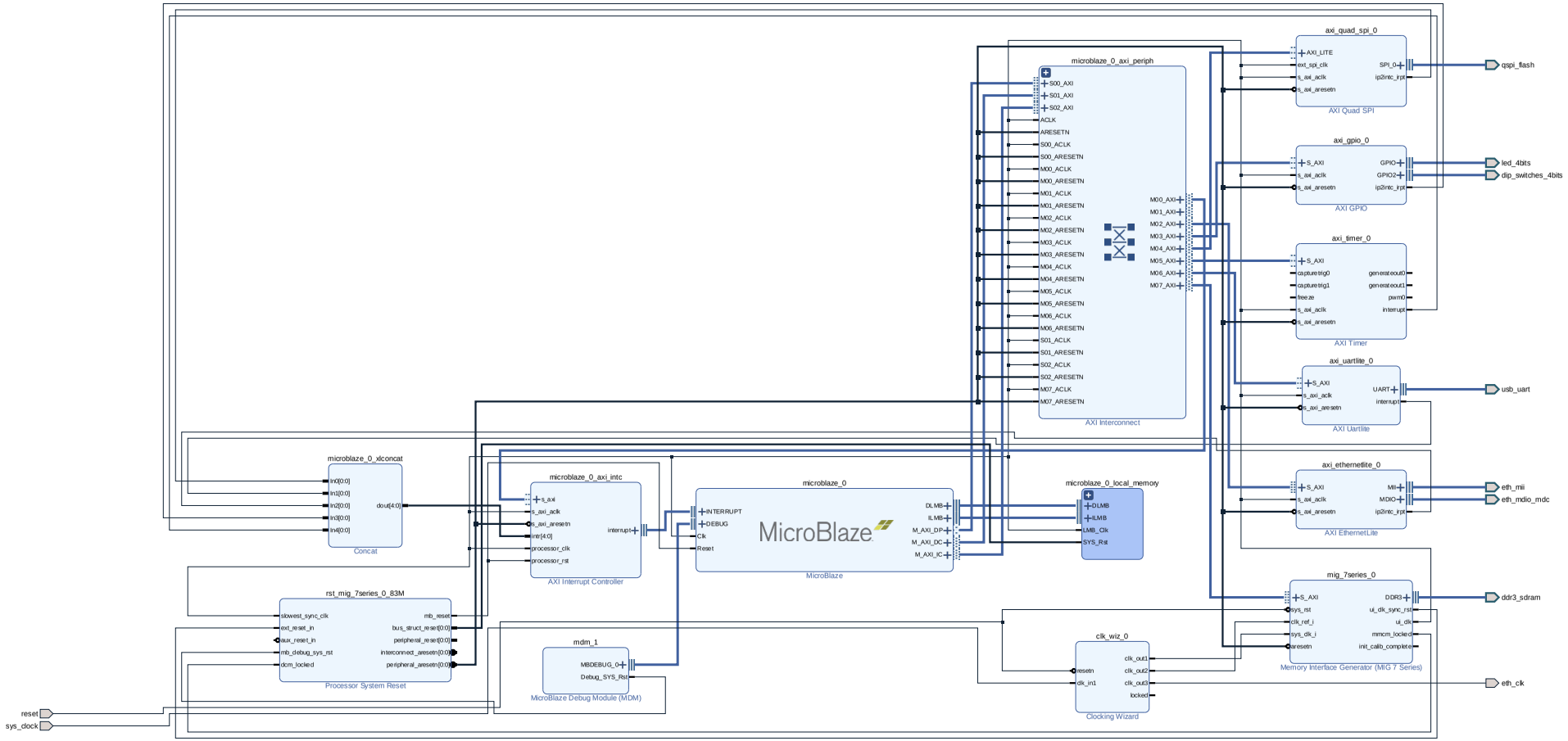
axi_quad_spi_0
AXI_LITE
ext_spi_clk
s_axi_aclk
s_axi_aresetn
SPI_0
ip2intc_irpt
AXI Quad SPI
qspi_flash
axi_gpio_0
S_AXI
s_axi_aclk
s_axi_aresetn
GPIO
GPIO2
ip2intc_irpt
AXI GPIO
led_4bits
dip_switches_4bits
axi_timer_0
S_AXI
capturetrig0
capturetrig1
freeze
s_axi_aclk
s_axi_aresetn
generateout0
generateout1
pwm0
interrupt
AXI Timer
axi_uartlite_0
S_AXI
s_axi_aclk
s_axi_aresetn
UART
interrupt
AXI Uartlite
usb_uart
axi_ethernetlite_0
S_AXI
s_axi_aclk
s_axi_aresetn
MII
MDIO
ip2intc_irpt
AXI EthernetLite
eth_mii
eth_mdio_mdc
mig_7series_0
S_AXI
sys_rst
clk_ref_i
sys_clk_i
aresetn
DDR3
ui_clk_sync_rst
ui_clk
mmcm_locked
init_calib_complete
Memory Interface Generator (MIG 7 Series)
ddr3_sdram
eth_clk
microblaze_0_axi_periph
S00_AXI
S01_AXI
S02_AXI
ACLK
ARESETN
S00_ACLK
S00_ARESETN
M00_ACLK
M00_ARESETN
M01_ACLK
M01_ARESETN
M02_ACLK
M02_ARESETN
M03_ACLK
M03_ARESETN
M04_ACLK
M04_ARESETN
M05_ACLK
M05_ARESETN
M06_ACLK
M06_ARESETN
S01_ACLK
S01_ARESETN
S02_ACLK
S02_ARESETN
M07_ACLK
M07_ARESETN
M00_AXI
M01_AXI
M02_AXI
M03_AXI
M04_AXI
M05_AXI
M06_AXI
M07_AXI
AXI Interconnect
microblaze_0_xlconcat
In0[0:0]
In1[0:0]
In2[0:0]
In3[0:0]
In4[0:0]
dout[4:0]
Concat
microblaze_0_axi_intc
s_axi
s_axi_aclk
s_axi_aresetn
intr[4:0]
processor_clk
processor_rst
interrupt
AXI Interrupt Controller
microblaze_0
INTERRUPT
DEBUG
Clk
Reset
DLMB
ILMB
M_AXI_DP
M_AXI_DC
M_AXI_IC
MicroBlaze
microblaze_0_local_memory
DLMB
ILMB
LMB_Clk
SYS_Rst
rst_mig_7series_0_83M
slowest_sync_clk
ext_reset_in
aux_reset_in
mb_debug_sys_rst
dcm_locked
mb_reset
bus_struct_reset[0:0]
peripheral_reset[0:0]
interconnect_aresetn[0:0]
peripheral_aresetn[0:0]
Processor System Reset
mdm_1
MBDEBUG_0
Debug_SYS_Rst
MicroBlaze Debug Module (MDM)
clk_wiz_0
resetn
clk_in1
clk_out1
clk_out2
clk_out3
locked
Clocking Wizard
reset
sys_clock